

# THE UNITED STATES PATENT AND TRADEMARK OFFICE

## TRANSMITTAL LETTER (Large Entity)

Applied A. Rumber: 09/190,208

Group Art Unit: 2643

Filed: November 13, 1998

Examiner Name: Lao

Applicant: Chen

Attorney Docket Number: Chen 3-1

TITLE:

METHOD AND APPARATUS FOR PROCESSING INTERAURAL TIME DELAY IN

3D DIGITAL AUDIO

Total Number of Pages in this Submission: 9

# DIRECTOR OF US PATENT AND TRADEMARK OFFICE WASHINGTON, DC 20231

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Technology Center 260c

SIR:

Transmitted herewith are an amendment in the above-identified application (9 Pages)

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED					
	CLAIMS	HIGHEST #	# OF	RATE	ADDITIONAL FEE
	REMAINING	PREV. PAID	EXTRA		
	AFTER Amendment	FOR	CLAIMS		
Total Claims	. 14	14	0	x \$18 =	\$ 0.00
Independent Claims	3	3	0	x \$84 =	\$ 0.00
Multiple Dependent				6260-	
Claim(s), if applicable				x \$280=	
		TOTAL ADDITIONAL FEE:			\$ 0.00

The Commissioner is hereby authorized to charge any additional fees required under 37 C.F.R. 1.16 or any patent application processing fees under 37 C.F.R. 1.17 associated with this communication, or credit any over payment to **Deposit Account No. 50-0687 under Order No. 73-852.** 

Respectfully submitted,

Willsam/ss. Bollman

Reg. No. 36,457

Attorney for Applicant(s)

Date: July 12, 2002

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**Technology Center 2600** 

Serial No.: 09/190,208

Filed: November 13, 1998

Group Art Unit: 2643 Examiner: Lao, Lun S.

Atty Docket No.: Chen 3-1

Our Ref.: 73-852

IN RE PATENT APPLICATION OF:

#### CHEN

METHOD AND APPARATUS FOR PROCESSING INTERAURAL TIME TITLE: **DELAY IN 3D DIGITAL AUDIO** 

July 12, 2002

### **AMENDMENT**

Director of the US Patent and Trademark Office Washington, D.C. 20231

Sir:

Responsive to the Office Action dated May 9, 2002, please enter the following amendments and remarks in the subject application:

#### IN THE CLAIMS:

Kindly amend the claim as follows:

#### Clean Set of Claims

comprising:

A digital delay line for use in a 3D audio sound system

a first delay module providing a choice of delay within a first resolution; and

a second delay module in series with said first delay module, said second delay module providing a choice of a plurality of additional fractional delays, each of said additional fractional delays being less than said first resolution.